

Workshop on Binary Translation Workshop (WBT-2001)

Saturday, September 8, 2001

Barcelona, Spain

Held in Conjunction with PACT'2001

<http://www.ac.upc.es/pact01/wbt2001.pdf>

8:30 - 8:45 OPENING REMARKS

8:45 - 9:45 KEYNOTE ADDRESS

“Dynamic Compilation: Panacea or Necessary Evil?”

– **Evelyn Duesterwald, HP Labs**

9:45 - 10:15 BREAK

10:15 - 11:45 SESSION 1: *Binary Translation*

“Strata: A Software Dynamic Translation Infrastructure,”

– **Kevin Scott** and **Jack Davidson**

“Fast Machine-Adaptable Dynamic Binary Translation,”

– **Mark Probst**

“Efficient Software Decoder Design,”

– **Rajeev Krishna** and **Todd Austin**

11:45 - 12:00 BREAK

12:00 - 13:30 SESSION 2: *Program and OS Optimization*

“PLTO: A Link-Time Optimizer for the Intel IA-32 Architecture,”

– **Benjamin Schwartz, Saumya Debray,** and **Gregory Andrews**

“Partial Data Traces: Efficient Generation and Representation,”

– **Frank Mueller, Tushar Mohan, Bronis de R. Supinski, Sally A. McKee,** and **Andy Yoo**

“Dynamic Kernel Code Optimization,”

– **Ariel Tamches** and **Barton P. Miller**

13:30

WORKSHOP ENDS

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